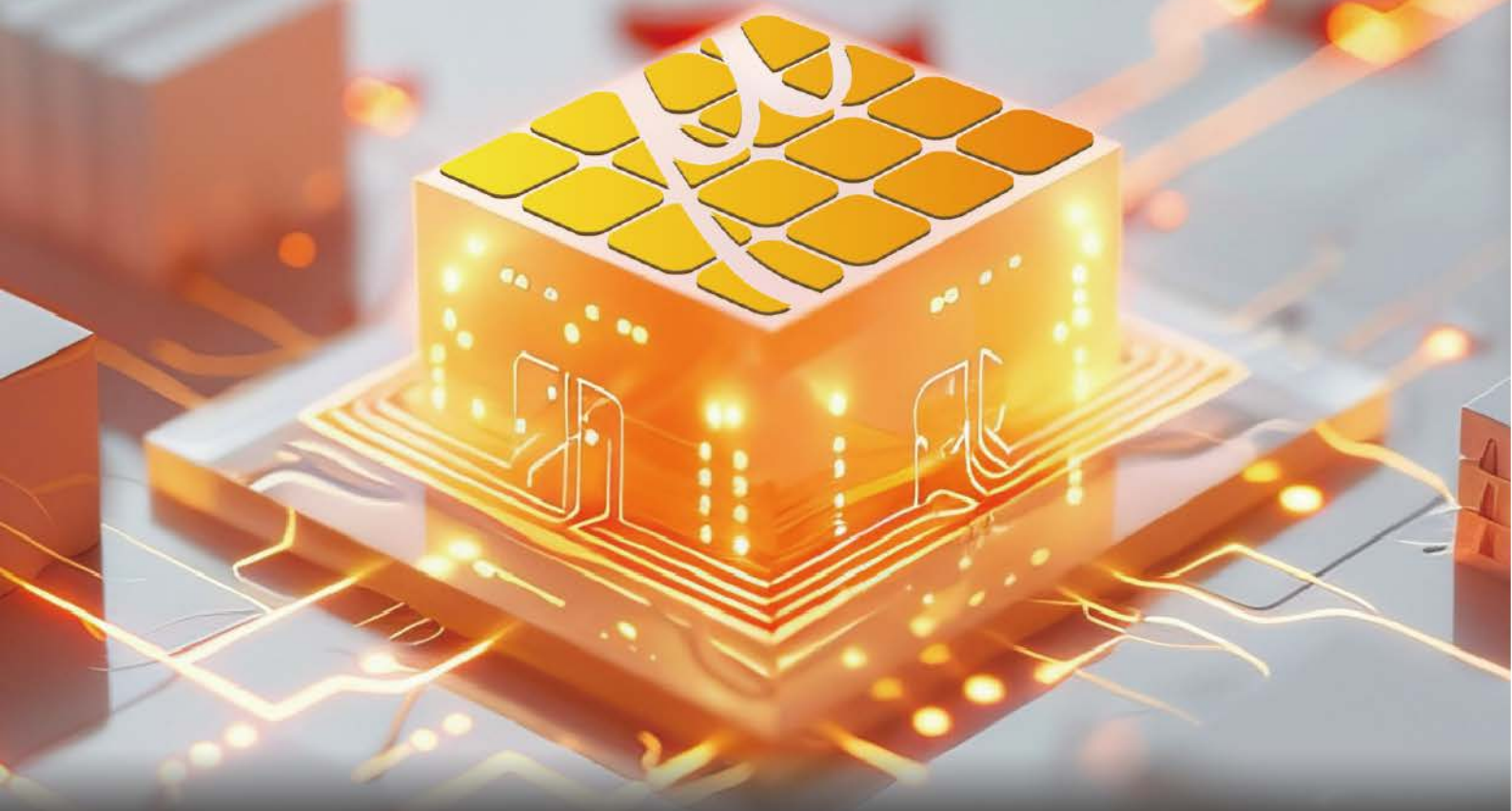# MICROIP

Why Choose MICROIP ?

- Comprehensive end-to-end IC design services — from idea-in, architecture to physical implementation.
- Extensive experience with multimedia, network security, image processing, wireless communication, and AI/ML SoC designs.
- Customized solutions to help reduce time-to-market and development risks.

Our Competitive Edge

- In-house iPROfiler™ Tool: Performance analysis at RTL levels for faster and more efficient design.
- Support for mainstream foundry nodes to ensure smooth integration and manufacturability.

✉ sales@micro-ip.com

CONTACT & FOLLOW US

LinkedIn   Facebook   Website

CATS V1.0

CATS
Custom ASIC Technology & Solutions

To ensure enterprises achieve greater design efficiency, lower power consumption, and improved performance to meet customized chip demands.
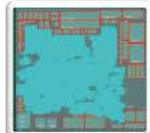
Our Proven Track Records

UMC
180nm 1P5M
Multimedia

tsmc
130nm 1P5M
Network Security
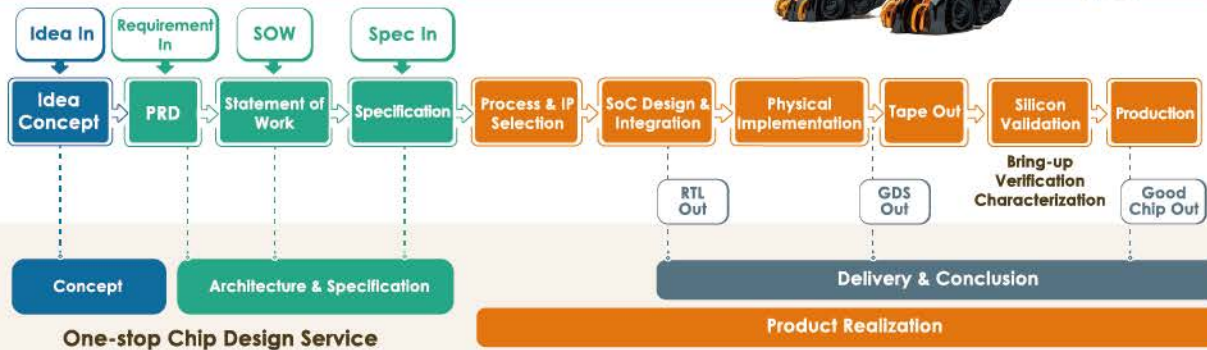
tsmc
40nm 1P7M
Image Processing

tsmc
22ULL 1P9M6X1Z1U
Vision w/ AI/ML NPU

From Concepts into Silicon, Mastering Mature Nodes with Expertise

Start Smart with MPW, Scale Big with Production Rewards

Idea In | **Requirement In** | **SOW** | **Spec In**

Idea Concept → PRD → Statement of Work → Specification → Process & IP Selection → SoC Design & Integration → Physical Implementation → Tape Out → Silicon Validation → Production

RTL Out | GDS Out | Bring-up Verification Characterization | Good Chip Out

Concept | **Architecture & Specification** | **Delivery & Conclusion**

One-stop Chip Design Service | **Product Realization**

*MPW: Multi-Project Wafer
*PRD: Product Requirement Document

Typical project design flow

Spec Freeze | Tape Out | Silicon | Project Finished

Arch. Design | Front-end Design | Back-end Design | Manufacturing | Software Development, Integration & Performance Analysis

Spec Freeze

MICROIP early time-to-market project design flow

Tape Out | Silicon | Project Finished

iPROfiler™

Arch. Design | F/E Design | B/E Design | Manufacturing | Software Development, Integration & Performance Analysis

Shift Left

Early Time-to-Market

IC verification and system development in RTL

Architecture Design Exploration | Verification / Debug
Performance Analysis

Reduce design time by **20% to 40%** with our proprietary iPROfiler™ EDA tool